

Notice of References Cited	Application/Control No. 09/892,893	Applicant(s)/Patent Under Reexamination IBARA ET AL.	
	Examiner W. David Coleman	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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FOREIGN PATENT DOCUMENTS

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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf Ph.D., "Silicon Processing for the VLSI Era", vol. 2, "Process Integration", Lattice Press 1990, pp 150-151
	V	
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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